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U.S. UTILITY Patent Application

PATENT NUMBER and  
ISSUE DATE

1145  
10/08/1479  
2/21/02

APPL NUM 10081479	FILING DATE 02/21/2002	CLASS 430	SUBCLASS 311	GAU 1756	EXAMINER Chacko-Davis
<b>**APPLICANTS:</b> Chiang Min-Hsiung; Lo Chi-Hsin;					
<b>**CONTINUING DATA VERIFIED:</b> <i>none</i>					
<b>** FOREIGN APPLICATIONS VERIFIED:</b> <i>none</i>					
PG-PUB <input type="checkbox"/> DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>			
Foreign priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no		35 USC 119 conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no		ATTORNEY DOCKET NO	
Verified and Acknowledged Examiners's Initials <i>DL</i>				67,200-576	
TITLE : Method for integrating copper process and MIM capacitor for embedded DRAM					
U.S. DEPT. OF COMM./PAT. & TM.-PTO-436L (Rev. 12-94)					

<b>NOTICE OF ALLOWANCE MAILED</b>		<b>CLAIMS ALLOWED</b>	
		Total Claims	Print Claim for O.G.
<b>ISSUE FEE</b>		<b>DRAWING</b>	
Amount Due	Date Paid	Sheets Drwg.	Figs. Drwg.
		Print Fig.	
<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>		<b>Application Examiner</b>	
		<b>PREPARED FOR ISSUE</b>	
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